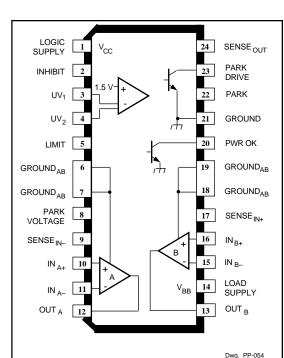
8958



ABSOLUTE MAXIMUM RATINGS at $T_{A} = 25^{\circ}C$

Supply Voltages, V _{BB} and V _{CC}
Continuous
Peak 1.0 A
Amplifier Input Voltage Range,
V _{IN} -2.0 V to V_{CC}
Sense Input Voltage Range,
V _{SENSE IN} 0.3 V to V _{CC}
Comparator and Digital Inputs,
V _{IN} -0.3 V to 10 V
Ι _{IN} ±10 mA
Power OK Output, V _{CEX} 20 V
I _C 30 mA
Output Clamp Diode Current,
I _F (pulsed) 1.0 A
Package Power Dissipation, P _D See Graph
Operating Temperature Range,
T _A 0°C to +70°C
Junction Temperature, T _J 150°C *
Storage Temperature Range,
T _S 55°C to +150°C
* Fault conditions that produce excessive junction temperature will activate device thermal shutdown

circuitry. These conditions can be tolerated but

should be avoided.

VOICE COIL MOTOR DRIVER

Providing control and drive of the voice coil motor used for head positioning in disk drive applications, the A8958CLB is a full-bridge driver which can be configured so that its output current is a direct function of an externally applied control voltage or current. This linear current control function is supplemented by additional circuitry to protect the heads and the data disk during system failure or normal system shutdown.

The two \pm 800 mA driver outputs provide very-low saturation voltage drops and precise current control utilizing a single current-sensing resistor connected in series with the load. Under-voltage lockout disables the system in a controlled sequence if a fault condition occurs.

When activated by the under-voltage comparator, or a park command, the output power drivers change from a controlled current to a user-determined constant park voltage. Other features include a power ok flag, a limit input to force the outputs to their maximum level in either polarity, an over-riding output disable to shut down both power amplifiers and reduce quiescent supply current, and internal thermal shutdown which disables the load (but still allowing the head to be parked) in the event of excessive junction temperatures. The load is re-enabled when the junction temperature returns to a safe level.

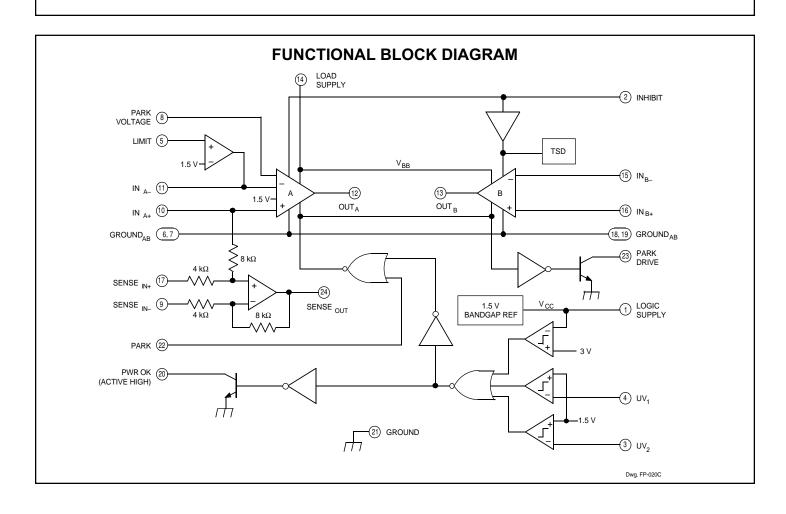
The A8958CLB is supplied in a 24-lead power SOIC for surfacemount applications. The copper batwing construction provides for maximum package power dissipation in a minimum package size. It is rated for continuous operation over the temperature range of 0° C to +70°C.

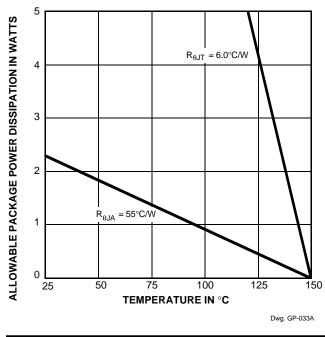
FEATURES

- Controlled-Velocity Head Parking
- Zero Deadband
- High Transconductance Bandwidth
- User-Adjustable Transconductance Gain
- ±800 mA Load Current
- Dual Under-Voltage Monitors with Flag and User-Selectable Trip Points
- Internal Thermal Shutdown Circuitry
- Replaces UC3175

Always order by complete part number, e.g., A8958CLB .









115 Northeast Cutoff, Box 15036 Worcester, Massachusetts 01615-0036 (508) 853-5000 Copyright © 1990, 2000 Allegro MicroSystems, Inc.

ELECTRICAL CHARACTERISTICS at $T_A = +25^{\circ}C$, $V_{CC} = V_{BB} = 12 V$

Characteristic	Symbol			Limits			
		Test Conditions	Min.	Тур.	Max.	Units	
Logic Supply Voltage Range	V _{CC}	Operating	8.0	12	16	V	
Logic Supply UV Threshold	V _{CC}	High-to-low transition	—	2.8	3.0	V	
Logic Supply UV Hysteresis	ΔV_{CC}		—	200	_	mV	
Supply Current	I _{BB}	V _{OUT} = 6 V, no load	—	2.0	—	mA	
	I _{CC}		—	23	—	mA	
Inhibited Supply Current	—	I_{BB} + I_{CC} , $V_2 \ge 1.7$ V	—	3.0	8.0	mA	
Thermal Shutdown Temp.	TJ		- 1	165	—	°C	
Thermal Shutdown Hysteresis	ΔT_{J}		- 1	8.0	—	°C	
Output Power Drivers			1			1	
Output Saturation Voltage	V _{SAT}	I _{OUT} = 250 mA	_	250	_	mV	
		I _{OUT} = 800 mA	—	450	—	mV	
		I _{OUT} = -250 mA	_	750	_	mV	
		I _{OUT} = -800 mA	_	950	_	mV	
Total Saturation Voltage (Source + Sink)	V _{SAT}	$I_{LOAD} = 250 \text{ mA}$	—	1.0	1.4	V	
		I _{LOAD} = 800 mA	—	1.4	2.0	V	
Input Offset Voltage	V _{IO}	V _{CM} = 6 V	—	5.0	8.0	mV	
Input Offset Drift	ΔV_{IO}		—	_	25	μV/°0	
Input Bias Current	l _{IN}	Except IN _{A+} , $V_{CM} = 6 V$	—	-150	-500	nA	
		IN_{A+} to $SENSE_{IN+} = 12 \text{ k}\Omega$, $T_J = 25^{\circ}C$	69	84	105	μΑ/\	
Input Offset Current	I _{IO}	IN_B only, $V_{CM} = 6 V$	- 1	_	200	nA	
Differential Sense Input Current	I _{ID}	I _{OUT} = 5 mA	—	±300		μΑ	
		I _{OUT} = 500 mA	—	3.0	—	mA	
Large Signal Gain	A _{VS}	V_{OUT} = 2 V to 10 V, I_{OUT} = ±500 mA	1.5	5.0	_	V/m\	
Slew Rate	SR		_	4.0	_	V/µs	
Unity Gain Bandwidth	BW	Amplifier A	0.5	1.0	1.7	MHz	
		Amplifier B	0.5	2.0	2.2	MHz	
Common-Mode Rejection	k _{CMR}	V _{CM} = 1 V to 10 V	70	90	_	dB	
Clamp Diode Forward Voltage	V _F	I_F = 800 mA, $V_2 \ge 1.7$ V	_	1.0	1.2	V	
High-Side Current Limit	I _{OUT}	$T_J = 25^{\circ}C$	_	1.0	1.2	Α	
Power Supply Rejection	k _{SVR}	$V_{CC} = 4 \text{ V to } 15 \text{ V}, \text{ V}_{CM} = 1.5 \text{ V}$	70	90	_	dB	

Negative current is defined as coming out of (sourcing) the specified device terminal.

Continued next page...

Typical Data is for design information only.

ELECTRICAL CHARACTERISTICS (continued)

	Symbol			Li	mits	
Characteristic		Test Conditions	Min.	Тур.	Max.	Units
Current Sense Amplifier						-
Input Offset Voltage	V _{IO}	V _{CM} = 6 V	—	_	2.0	mV
Input Offset Drift	ΔV_{IO}	$V_{CM} = 0 V \text{ to } 12 V$	—	_	3000	μV/V
			—	—	8.0	μV/°C
Voltage Gain	A _{VS}	$V_{ID} = -1 V \text{ to } +1 V, V_{CM} = 6 V$	1.95	2.00	2.05	_
Output Saturation Voltage	V _{SAT}	V _{OUT} , I _{OUT(SINK)} = 1.5 mA	-	300	500	mV
		V_{CC} - V_{OUT} , $I_{OUT(SOURCE)}$ = -1.5 mA	—	400	700	mV
Park Function						•
PARK DRIVE Leakage Current	I _{CEX}	V _{CEX} = 20 V	—	—	100	μA
PARK DRIVE Saturation Voltage	V _{CE(SAT)}	I _C = 200 mA	—	300	500	mV
PARK Input Threshold	V _{PARK}		0.7	1.1	1.7	V
PARK Input Current	I _{PARK}	V _{PARK} = 1.7 V	_	_	100	μA
PARK VOLTAGE Input Current	I _{PARK V}		—	-150	-500	nA
Under-Voltage Protection	-		-			
UV Threshold	V _{UV}	Low-to-High Trans., Other Input = 6 V	1.48	1.50	1.52	V
UV Threshold Hysteresis	ΔV_{UV}		15	25	45	mV
UV Input Current	l _{UV}	V _{UV} = 1 V	—	-0.5	-1.5	μA
PWR OK Saturation Voltage	V _{CE(SAT)}	$I_{\rm C} = 5 \rm{mA}$	—	_	450	mV
PWR OK Leakage Current	I _{CEX}	V _{CEX} = 20 V	—	—	5.0	μΑ
Auxiliary Functions			•			•
LIMIT Input Voltage	V _{LIMIT(L)}	OUT _A forced Low	0.7	0.8	_	V
	V _{LIMIT(H)}	OUT _A forced High	-	2.2	2.3	V
	V _{LIMIT}	Limit inactive	1.2	—	1.8	V
		Open circuit	1.45	1.50	1.55	V
LIMIT Input Resistance	R _{LIMIT}	V _{LIMIT} = 1.2 V to 1.8 V	-	10	_	kΩ
INHIBIT Input Threshold	V ₂		0.7	1.1	1.7	V
INHIBIT Input Current	l ₂	V ₂ = 1.7 V	—	_	200	μA

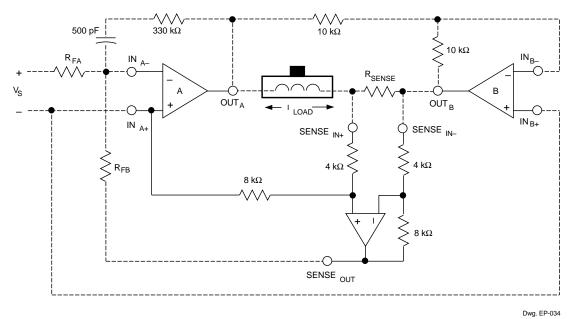
Negative current is defined as coming out of (sourcing) the specified device terminal.

Typical Data is for design information only.



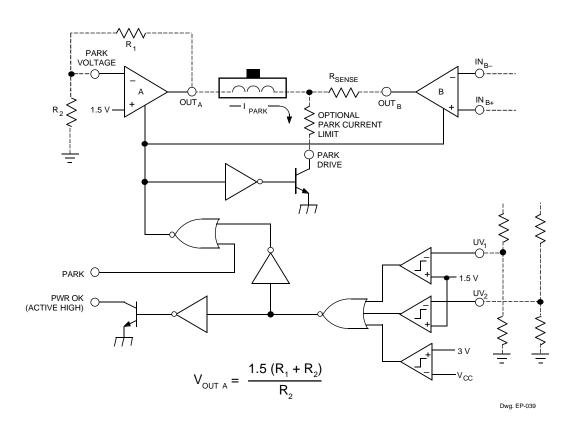
TERMINAL FUNCTIONS

Term.	Terminal Name	Function		
1	LOGIC SUPPLY	V _{CC} ; logic supply voltage.		
2	INHIBIT	An active-high logic input that inhibits the output stages without initiating a park.		
3 & 4	UV_1 and UV_2	Under-voltage detection inputs. If not used, these terminals must be connected to the logic supply (V _{CC}).		
5	LIMIT	A tri-state input that forces the output of amplifier A into saturation in either direction, or allows normal linear operation.		
6 & 7	GROUND _{AB}	Power amplifiers' ground and thermal heat sink.		
8	PARK VOLTAGE	Auxiliary inverting input to power amplifier A.		
9	SENSE _{IN-}	Inverting input to current sense error amplifer.		
10	IN _{A+}	Non-inverting input to power amplifier A.		
11	IN _{A-}	Inverting input to power amplifier A.		
12	OUT _A	Power amplifier A output to voice coil motor.		
13	OUT _B	Power amplifier B output to voice coil motor.		
14	LOAD SUPPLY	V _{BB} ; load supply voltage.		
15	IN _{B-}	Inverting input to power amplifier B.		
16	IN _{B+}	Non-inverting input to power amplifier B.		
17	SENSE _{IN+}	Non-inverting input to current sense error amplifer.		
18 & 19	GROUND _{AB}	Power amplifiers' ground and thermal heat sink.		
20	PWR OK	A logic low at this output indicates an under-voltage condition.		
21	GROUND	Circuit reference.		
22	PARK	An active-high logic input that activates the park function.		
23	PARK DRIVE	Power transistor for retract current control on power down or park command.		
24	SENSE _{OUT}	Output of current sense error amplifier.		



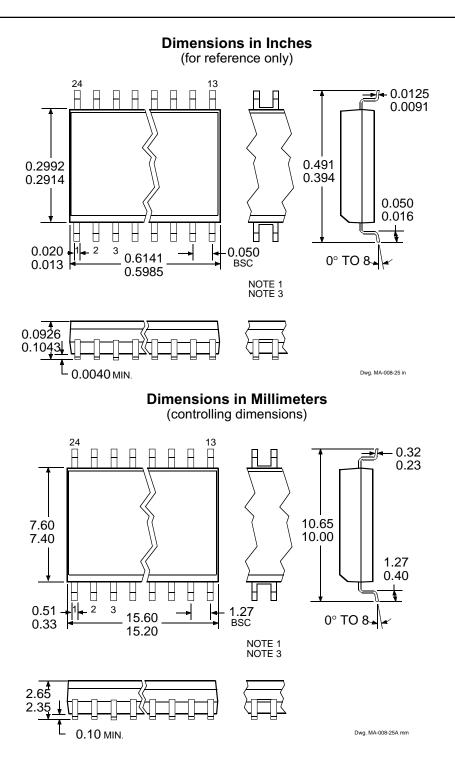
CURRENT SENSING







115 Northeast Cutoff, Box 15036 Worcester, Massachusetts 01615-0036 (508) 853-5000



NOTES: 1. Webbed lead frame. Leads 6, 7, 18, and 19 are internally one piece.

- 2. Lead spacing tolerance is non-cumulative.
- 3. Exact body and lead configuration at vendor's option within limits shown.

The products described here are manufactured under one or more U.S. patents or U.S. patents pending.

Allegro MicroSystems, Inc. reserves the right to make, from time to time, such departures from the detail specifications as may be required to permit improvements in the performance, reliability, or manufacturability of its products. Before placing an order, the user is cautioned to verify that the information being relied upon is current.

Allegro products are not authorized for use as critical components in life-support devices or systems without express written approval.

The information included herein is believed to be accurate and reliable. However, Allegro MicroSystems, Inc. assumes no responsibility for its use; nor for any infringement of patents or other rights of third parties which may result from its use.



115 Northeast Cutoff, Box 15036 Worcester, Massachusetts 01615-0036 (508) 853-5000